

*fig. 2*

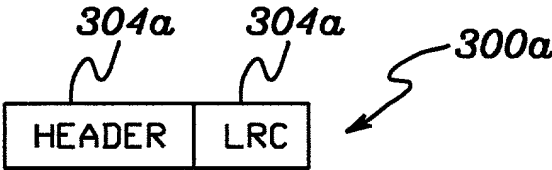


fig. 3a

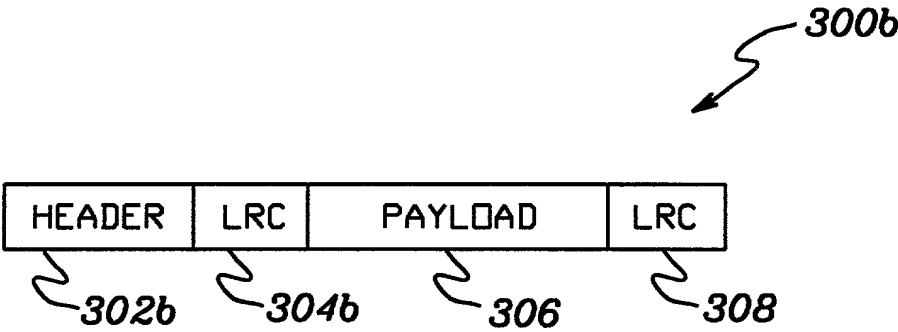


fig. 3b

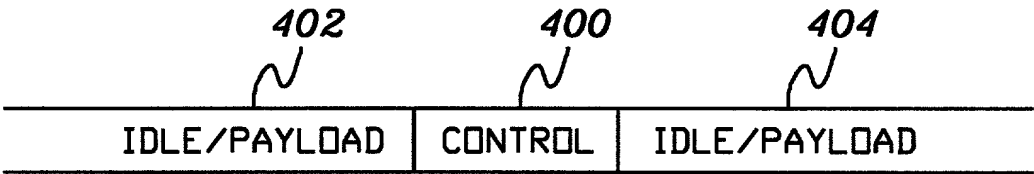


fig. 4

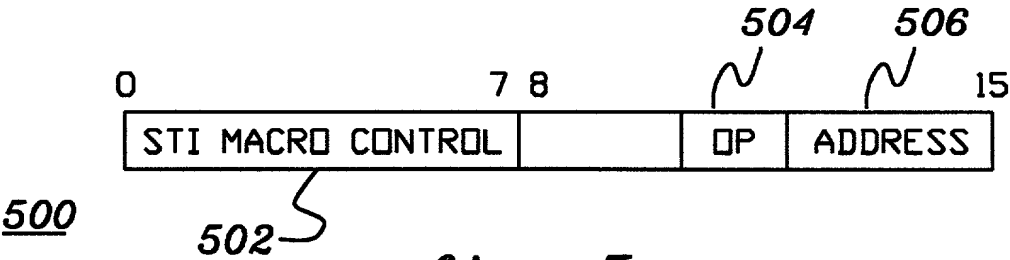
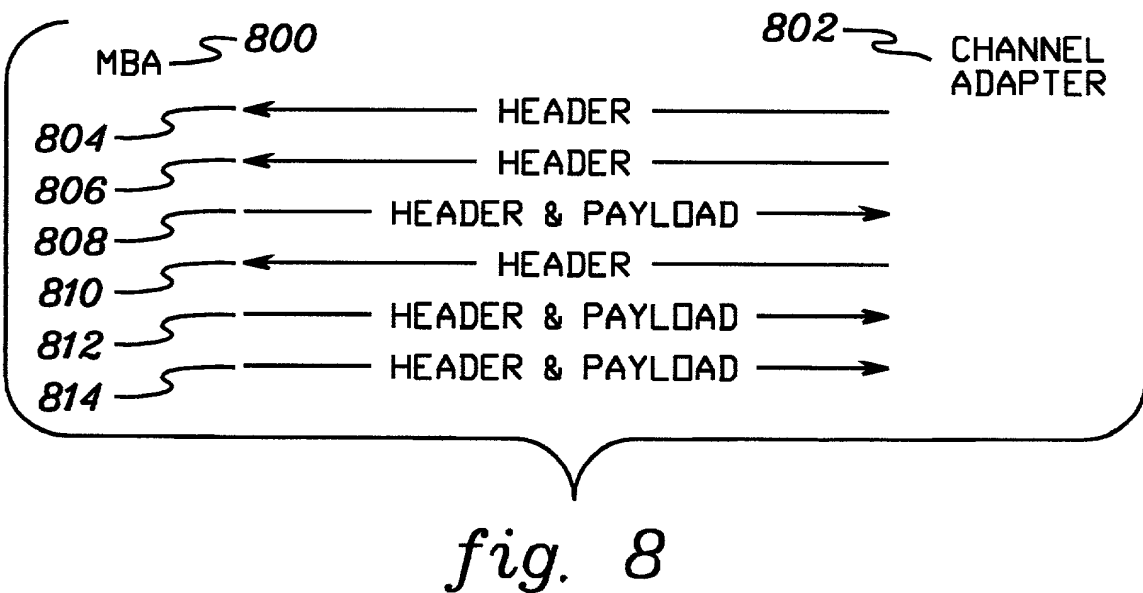
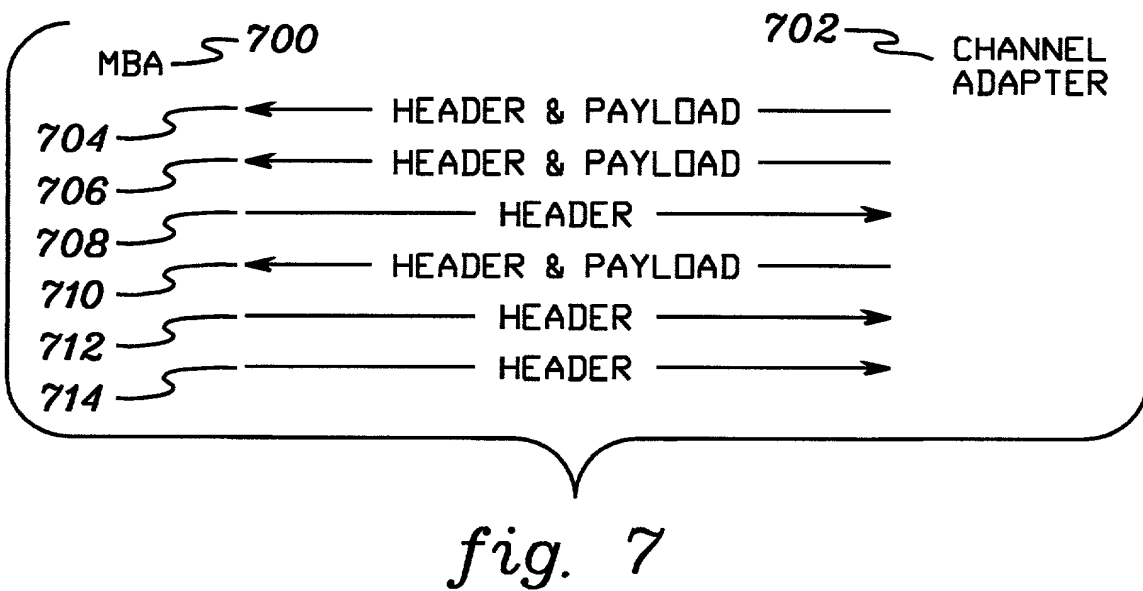
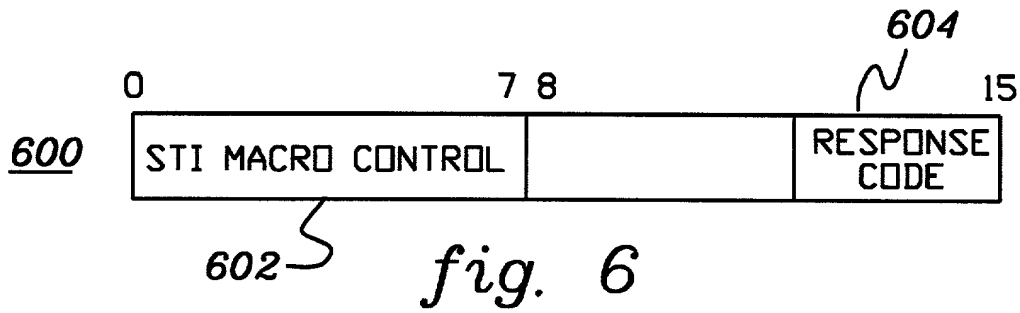
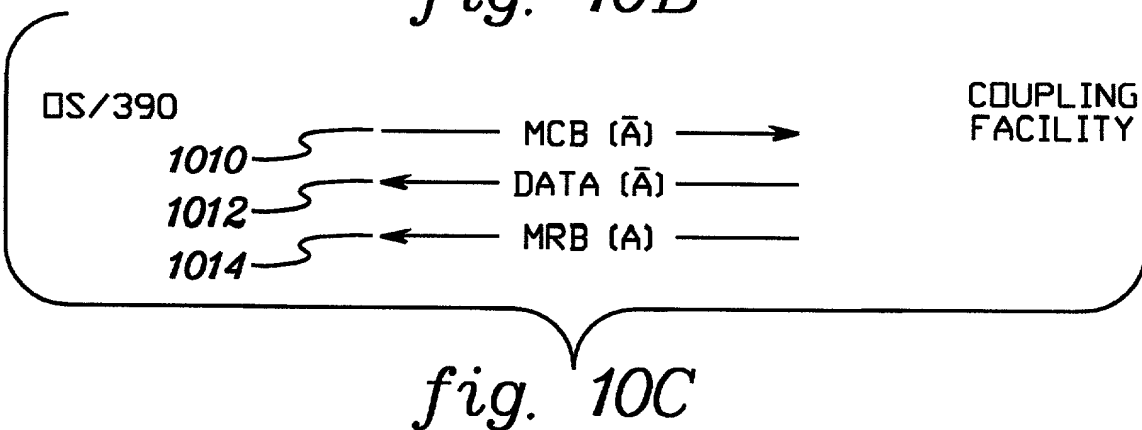
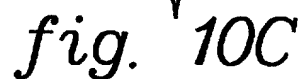
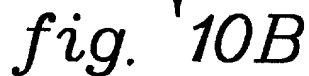
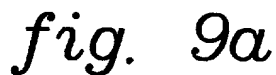
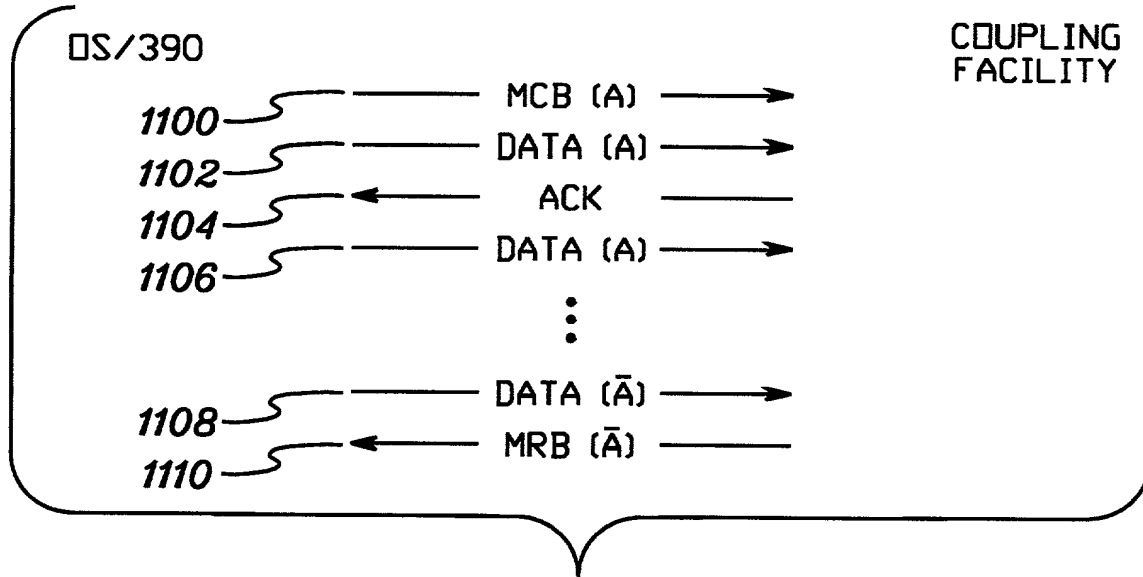


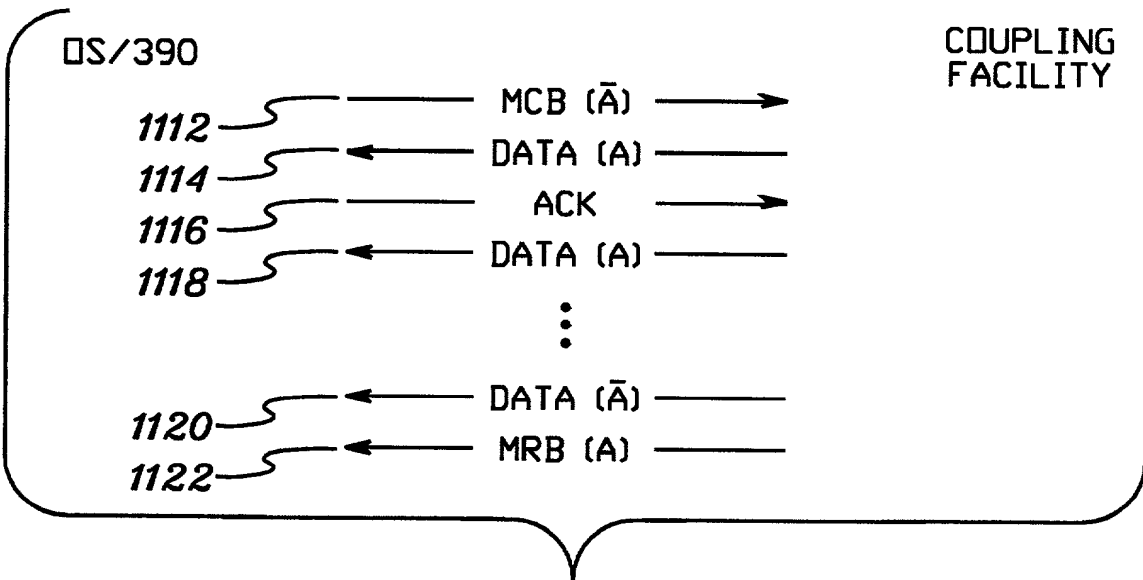
fig. 5





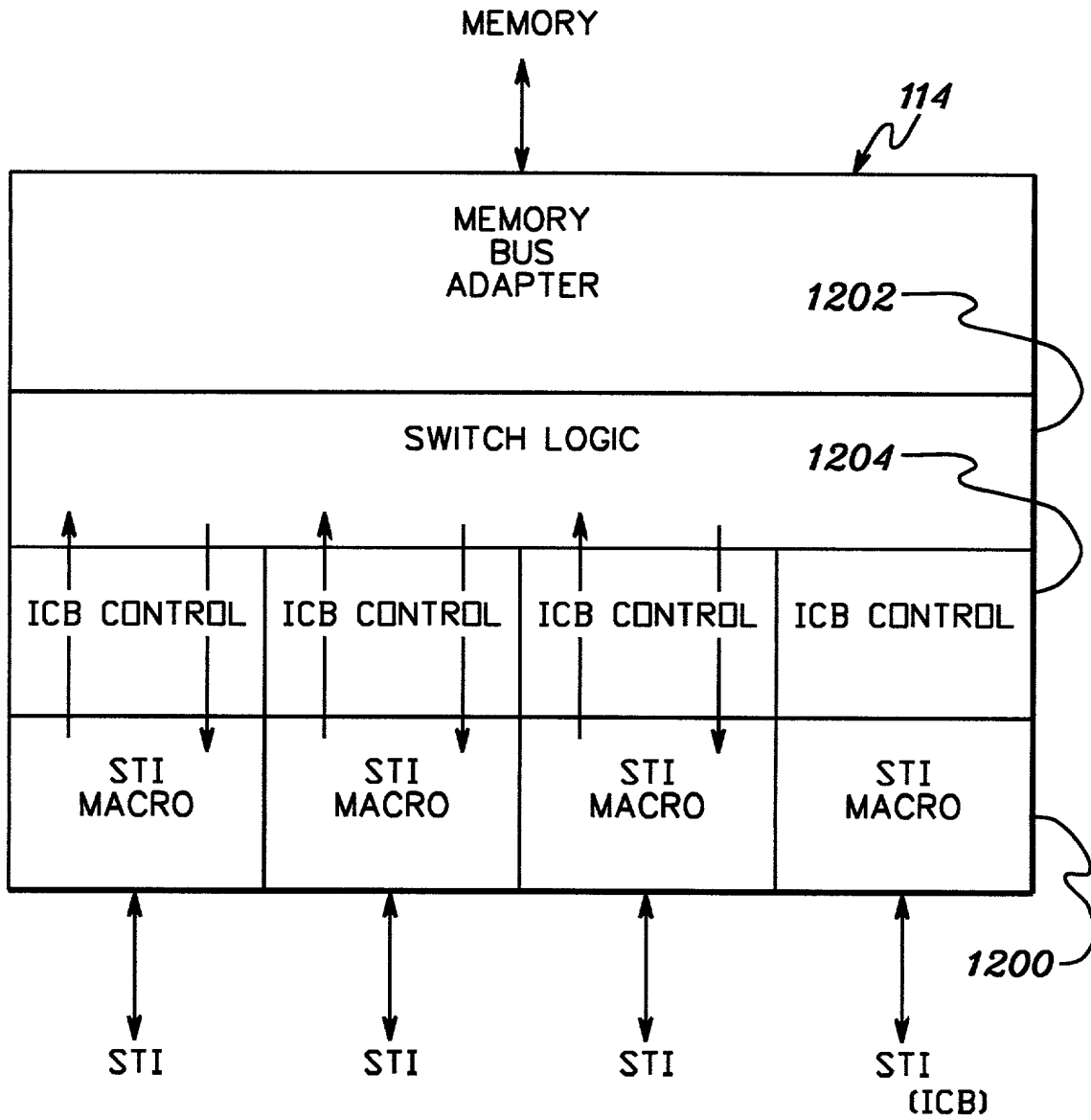


*fig. 11A*



*fig. 11B*

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*fig. 12*

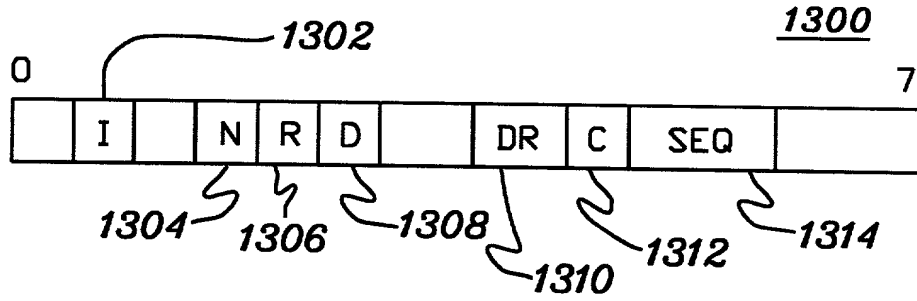


fig. 13

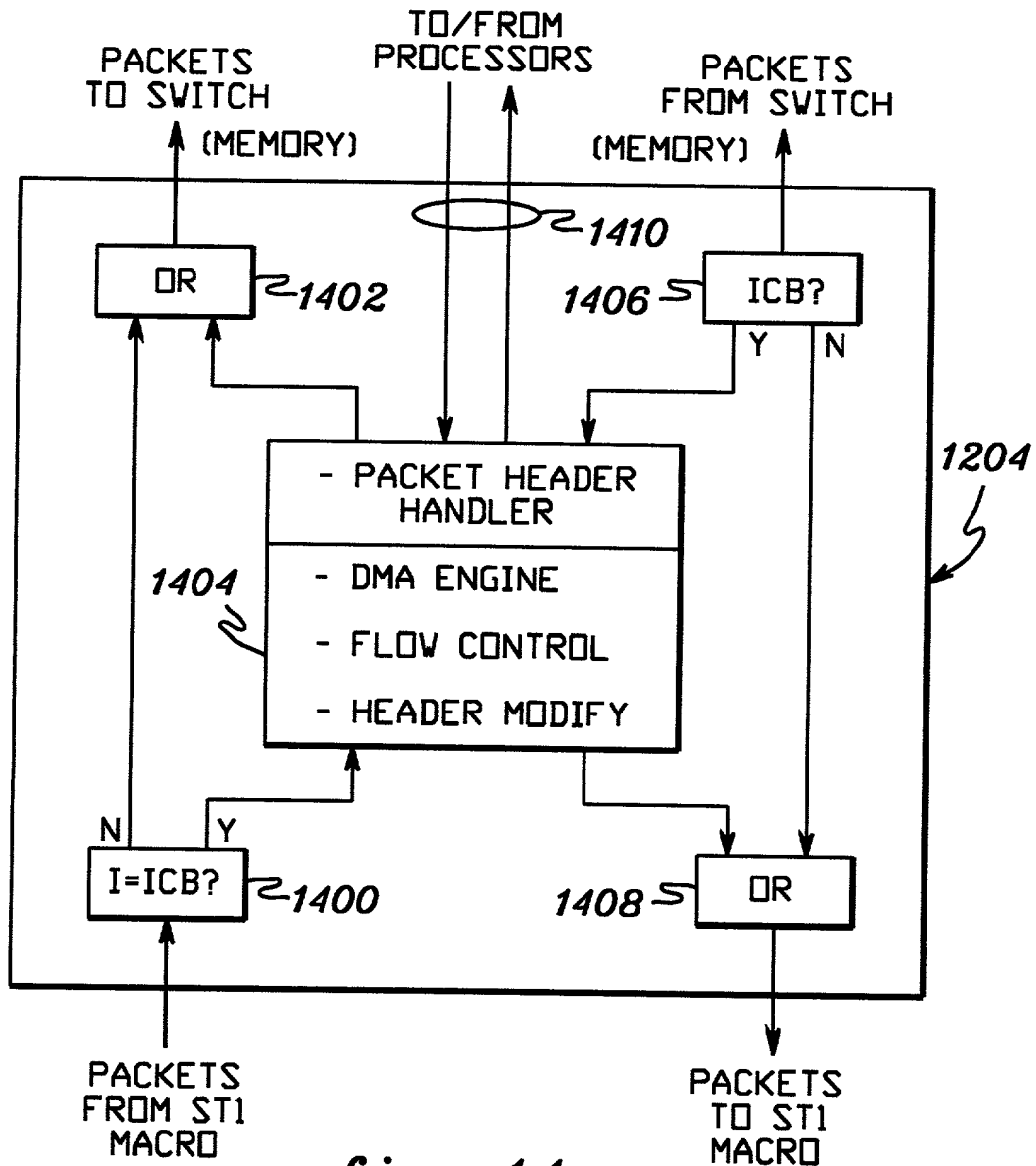


fig. 14

FIG. 14 is a block diagram of a switch architecture.



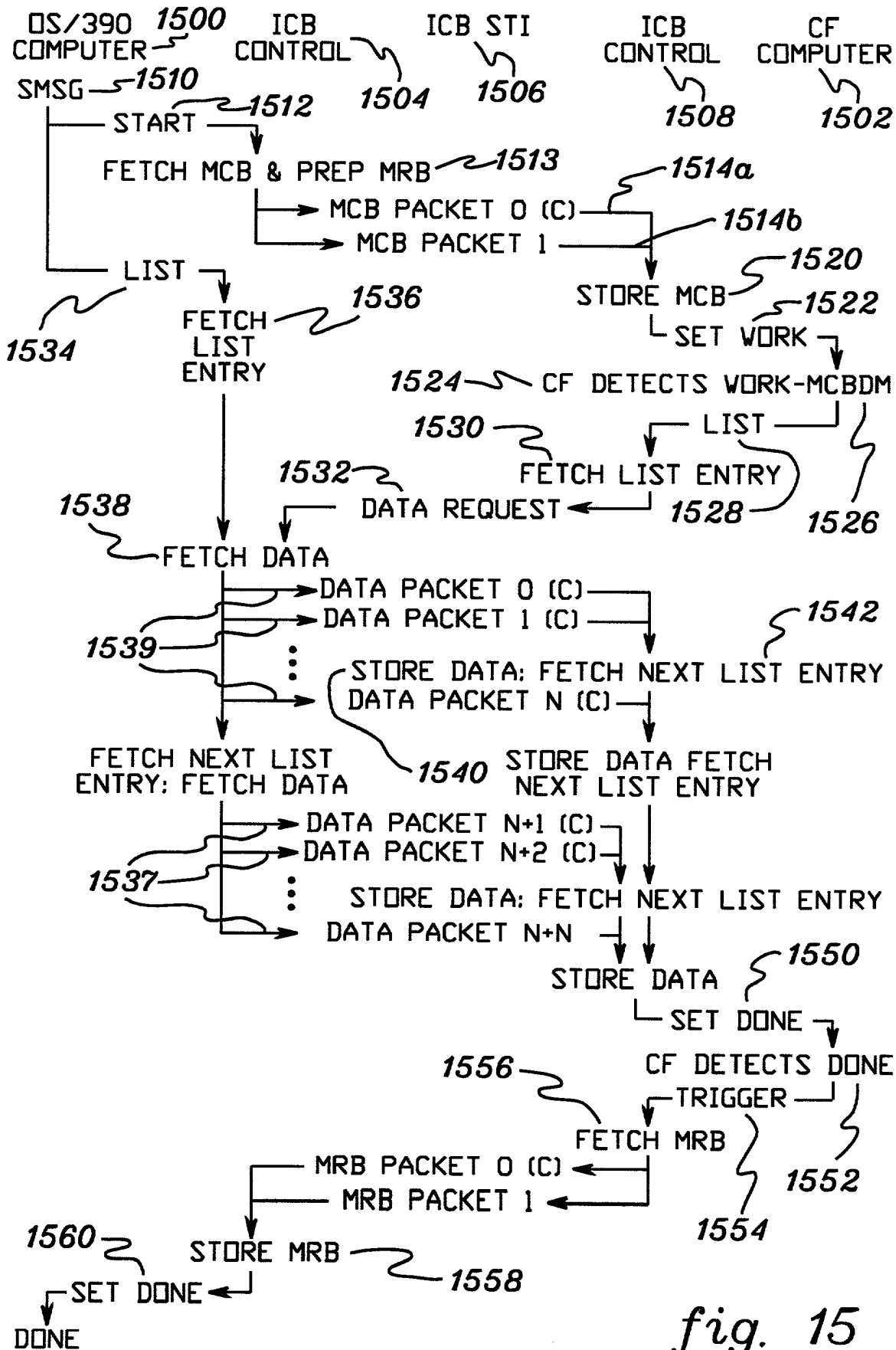


fig. 15

09374433 030504



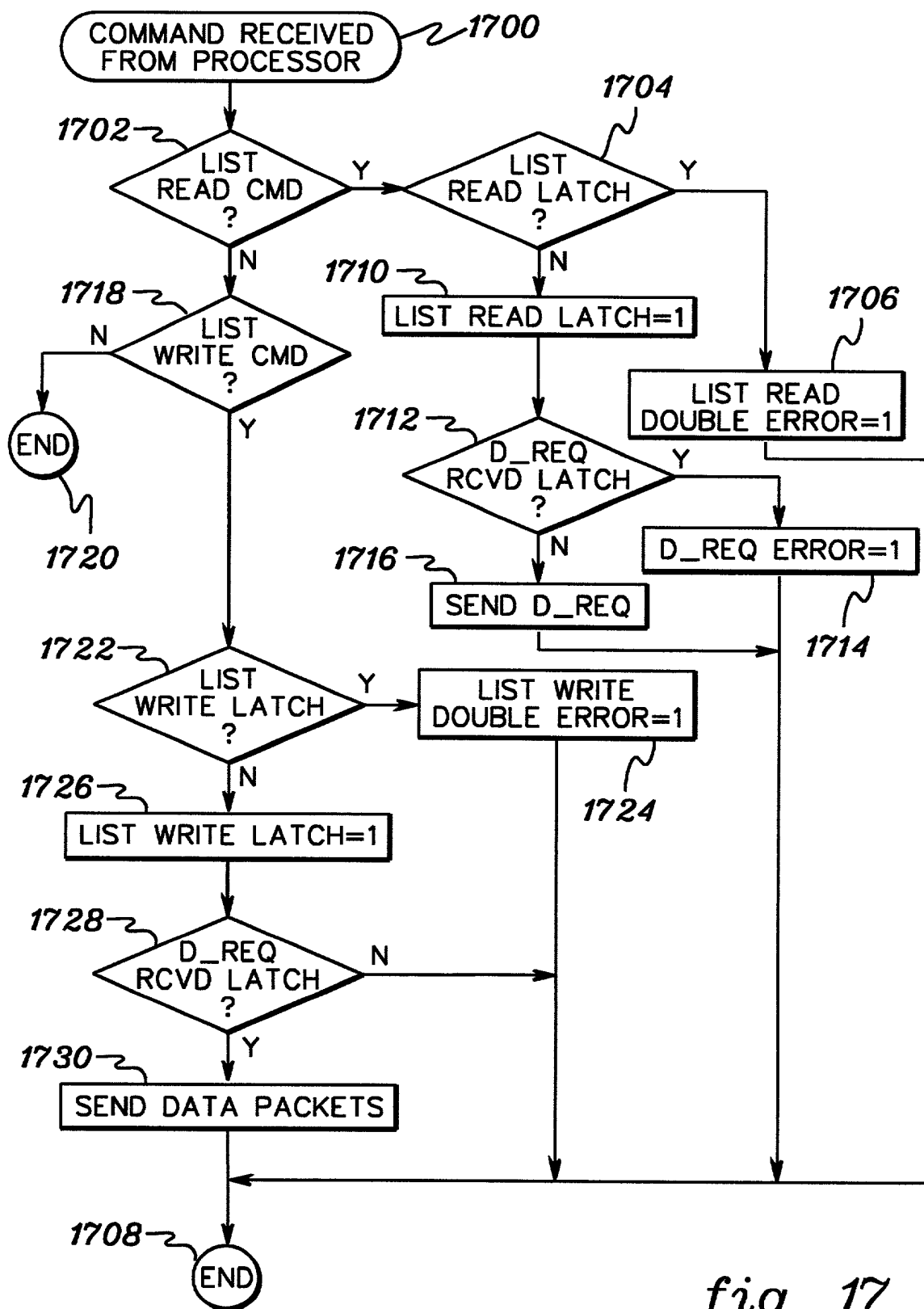
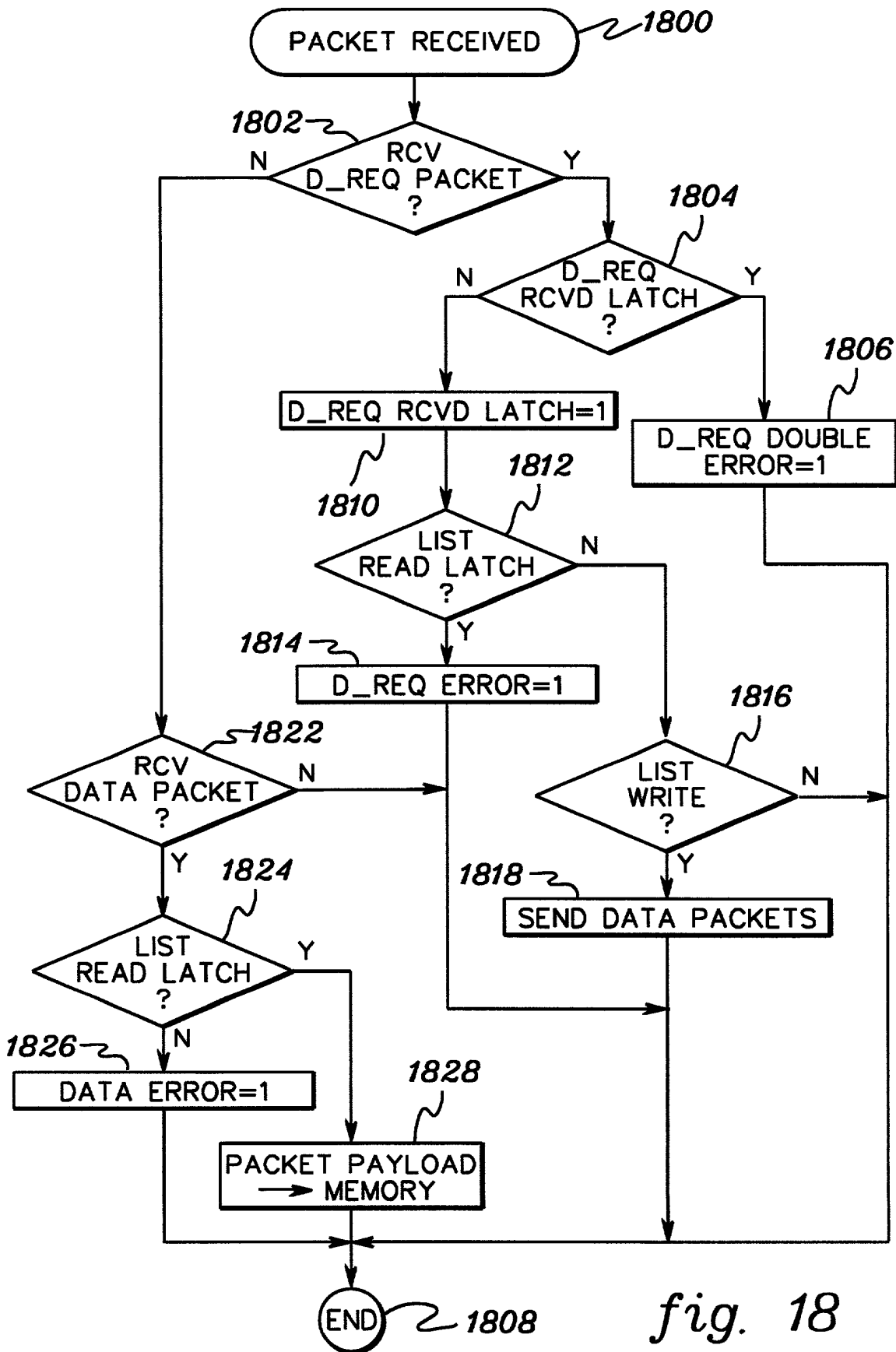


fig. 17



*fig. 18*

105090" 88442850

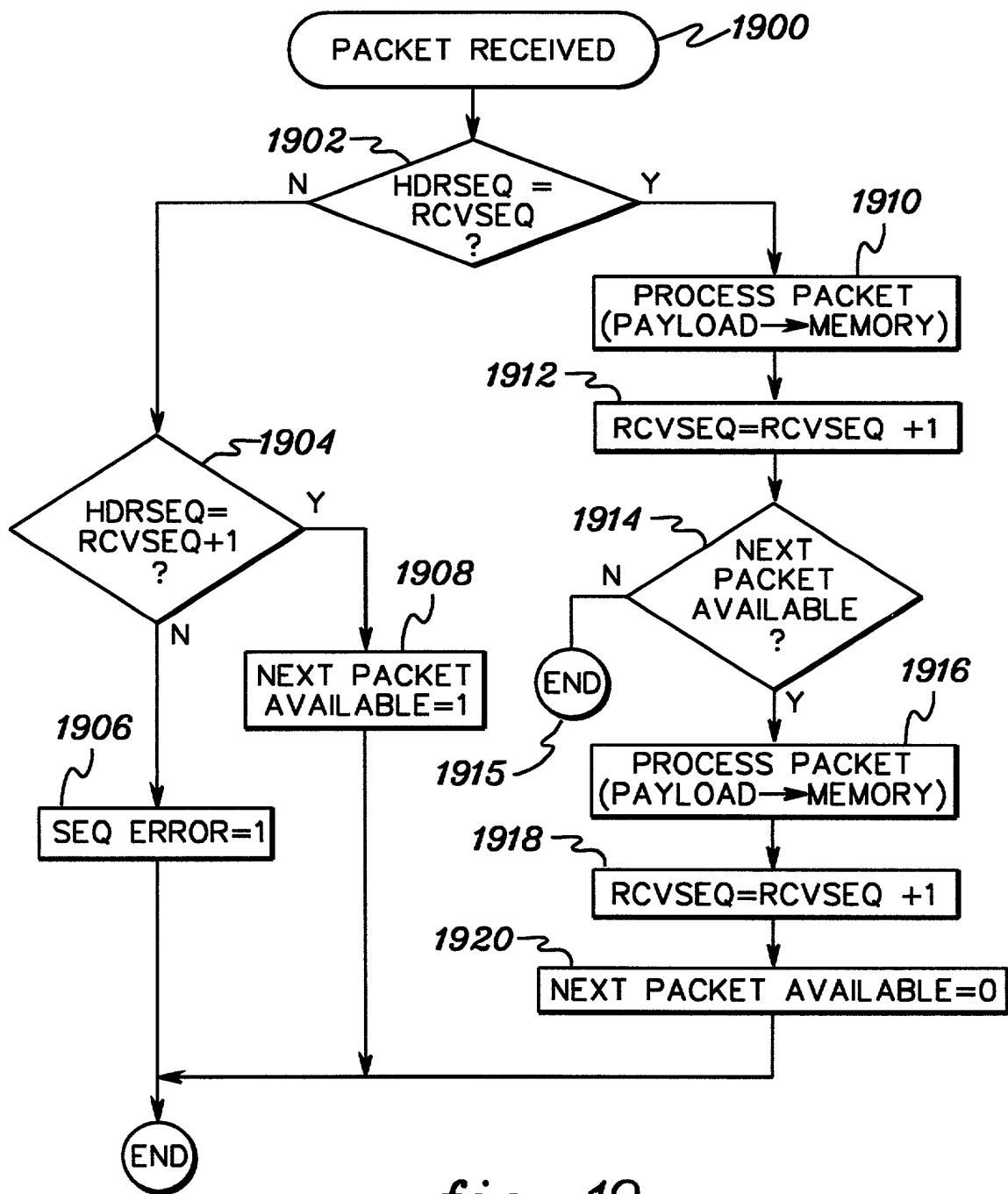


fig. 19

105090" 88472850

